



# तेजपुर विश्वविद्यालय

शैक्षिक सत्र 2008-10 में संविधियों तथा अध्यादेशों  
में विभिन्न सभी आवश्यकताओं को  
सफलतापूर्वक पूर्ण करने के उपरान्त  
**चुसेन दुवरी**

को

भारतीय गणराज्य के तेजपुर (असम) स्थित विश्वविद्यालय  
की मुद्रा के अधीन इलेक्ट्रॉनिक्स अभिकल्प एवं प्रौद्योगिकी में  
मास्टर ऑफ टेक्नोलॉजी  
की उपाधि दिनांक 10 फरवरी, 2012 को प्रदान करता है।

## Tezpur University

on successful completion of all the requirements as specified in the  
Statutes and Ordinances, in the academic session 2008-10

has conferred upon

**CHUSEN DUARI**

the Degree of

**Master of Technology**

in

**Electronics Design and Technology**

given under the seal of the University at  
Tezpur, Assam in the Republic of India on the  
10th Day of February, 2012

*Chusen Duari*



03/2009/ 032

*A. L. Anand*  
कुलसचिव  
Registrar

*Chusen Duari*  
कुलपति  
Vice-Chancellor



**TEZPUR UNIVERSITY**  
NAPAAM : TEZPUR : 784028  
ASSAM : INDIA

**PROVISIONAL PASS CERTIFICATE**

Enrolment No : ELD08018  
Name : CHUSEN DUARI  
Programme : M TECH (ELECTRONICS DESIGN AND TECHNOLOGY)  
Department : ELECTRONICS AND COMMUNICATION ENGINEERING  
Sch. of : ENGINEERING  
Degree : M TECH (ELECTRONICS DESIGN AND TECHNOLOGY)

Certified that he/she has satisfactorily completed all the requirements for the award of the DEGREE in the SPRING 2010 semester securing a CGPA of 7.67 and has been placed in 1st Class.

His/Her character and conduct have been found to be satisfactory.

Date : 06.07.2010.

  
Controller of Examinations

Chusen Duari





No. T-1 / 2009 /

0377

# TEZPUR UNIVERSITY

## TRANSCRIPT

Roll No. : 8508018  
 Name : CHUSEN DWARI  
 Programme : M.TECH (ELECTRONICS DESIGN AND PACKAGING)  
 Department : ELECTRONICS AND COMMUNICATION ENGINEERING

Year of Admission : 2008

Sl. No.	Year	Course Code	Course Title	Credit	Grade	CGPA	CGPA
1.	2008 AUTUMN	EL551	FUNDAMENTAL AND EXPERIMENTAL DESIGN OF ELECTRONICS SYSTEMS	4	B+	7	7
		EL519	DESIGN OF DIGITAL SYSTEMS	5	B		
		EL521	DESIGN AND TECHNOLOGY OF ELECTRONIC DEVICES	4	B		
		EL522	ADVANCED PROGRAMMING LANGUAGE	3	B		
		EL525	DATA COMMUNICATION AND NETWORKS	3	C+		
		EL529	SEMINAR - I	1	B+		
2.	2009 SPRING	EL518	DESIGN OF FINE MECHANICS AND POWER DEVICES	4	B	7	7
		EL516	VLSI DESIGN	3	B		
		EL520	QUALITY AND RELIABILITY ENGINEERING	4	C+		
		EL523	INTELLIGENT INSTRUMENTATION	5	A		
		EL524	HEATING AND INSULATION	3	C+		
		EL528	SEMINAR - II	1	C+		
3.	2009 AUTUMN	EL601	M. TECH. DISSERTATION	24	A	8	7
4.	2010 WINTER	EL601	M. TECH. DISSERTATION	24	A	8	7.67

UGRAA : I

*Chusen Dwari*

Verified by

Date : 11.01.2011.

  
 Controller of Examinations

Please see overleaf for Grading System